**Winter Internship Program 2012**

**Department Of Electronics**

**Leading Electronic Applications & Design Researchers Setup (LEADERS)**

**(17th December 2012 - 6th January 2013)**

**REGISTRATION FORM**

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| --- |
| Internee Information |
| Name: |            | Roll Number: |            |
| E-Mail Address: |            | Contact no: |            |
| Address: |             |  |  |
|  |
|  |
| Area Of Interest  |

Area of Interest (Double Click any one checkbox of your interested field to switch between checked or unchecked):

[x] Project Based Learning (PBL) [ ]  Microcontroller & Microprocessor

[ ] FPGAs [ ]  Digital Electronics

[ ] DSP

In case of PBL please give brief description of your project in the space provided below

[Edit Text here]

*N.B: Email filled forms at* *info@es.muet.edu.pk*