



## **ANNOUNCEMENT OF RESULT**

### **SECOND TERM SECOND YEAR (4<sup>TH</sup> TERM) OF 10-BATCH**

**SUPPLEMENTARY EXAMINATION HELD IN THE MONTH OF AUGUST / SEPTEMBER, 2014.**

### **DEPARTMENT OF ELECTRONIC ENGINEERING**

Candidates having the following Identification Numbers (ID. Nos.) are declared Successful with the Total Marks secured out of **1350**.

<b>ID. #.</b>	<b>Marks Secured</b>	<b>ID. #.</b>	<b>Marks Secured</b>
10ES104	783	10ES118	681
10ES108	738	10-09ES67	707

Candidates having the following Identification Numbers (ID. Nos.) have passed only 2<sup>nd</sup> Term 2<sup>nd</sup> Year.

<b>ID. #.</b>	<b>Marks Secured</b>	<b>ID. #.</b>	<b>Marks Secured</b>
10ES106	344-APR	10ES126	340-APR

<b>LIST OF FAILURE(S)</b>	
<b>ID. NO.</b>	<b>SUBJECT(S)</b>
10ES30	Sequential Circuits Design (Th) and Complex Variables & Transforms.
10ES130	Sequential Circuits Design (Th) and Complex Variables & Transforms.
10ES137	Sequential Circuits Design (Th) and Complex Variables & Transforms.
10ES138	Sequential Circuits Design (Th) and Complex Variables & Transforms.

### **NOTE:-**

1. The Marks Certificate of all concerned students who have paid prescribed fee along-with Examination forms shall be sent to the department within due course of time.
2. Error/Omissions, if any occurred inadvertently can be rectified at any stage on the basis of original record.
3. The University reserves the right of issuing any correction in the result, if any mistake is detected later.
4. The date of announcement of result is: **28 – 11 – 2014**

**JAMSHORO,**  
**DATED:- 2<sup>nd</sup> December, 2014**

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**CONTROLLER OF EXAMINATIONS**

A. A. Panhwar