The Schedule of **Mid Semester Examination** of 2nd Semester 14th Batch Master of Engineering (Full Time Evening) in various disciplines of Institute of Information & Communication Technologies (I.I.C.T), to be held from **Monday, 09th March, 2015** respectively in **I. T. Building**, at Mehran University of Engineering & Technology, Jamshoro and Mehran University of Engineering & Technology, SZA Bhutto Campus, Khairpur Mir's is given as under:

<table>
<thead>
<tr>
<th>DATE &amp; DAY</th>
<th>TIME</th>
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<tbody>
<tr>
<td>09-03-2015 Monday</td>
<td>3:00 PM to 4:30 PM</td>
<td>Energy Management (for 2-credit hours)</td>
<td>Modern Control Systems (for 3-credit hours)</td>
<td>Microelectronics (for 3-credit hours)</td>
<td>Computer Vision (for 3-credit hours)</td>
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<tr>
<td>10-03-2015 Tuesday</td>
<td>3:00 PM to 4:30 PM</td>
<td>Power System Analysis (for 2-credit hours)</td>
<td>Mechanics of Robot Manipulators (for 2-credit hours)</td>
<td>Advanced Digital Design (for 3-credit hours)</td>
<td>Embedded Operating System (for 3-credit hours)</td>
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<tr>
<td>11-03-2015 Wednesday</td>
<td>3:00 PM to 4:30 PM</td>
<td>Power Electronics &amp; Motor Drives (for 3-credit hours)</td>
<td>Computational Intelligence (for 3-credit hours)</td>
<td>Power Electronics &amp; Drives (for 3-credit hours)</td>
<td>Control Systems with Embedded Implementation (for 2-credit hours)</td>
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<tr>
<td>12-03-2015 Thursday</td>
<td>3:00 PM to 4:30 PM</td>
<td>Power System Stability (for 2-credit hours)</td>
<td>Embedded Systems and Applications (for 2-credit hours)</td>
<td>Communication Systems (for 3-credit hours)</td>
<td>Digital Signal Processing using FPGA (for 2-credit hours)</td>
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<tr>
<td>13-03-2015 Friday</td>
<td>3:00 PM to 4:30 PM</td>
<td>Power System Protection (for 2-credit hours)</td>
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- 3:00PM to 4:30 PM = 3-Credit Hours
- 3:00PM to 4:00 PM = 2-Credit Hours

**NOTE:**

1. Eligible candidates are required to present themselves at the place of Examination 15 minutes before the time fixed for Examination.
2. Eligible candidates are required to bring their **ADMIT SLIPS** at time of Examination.
3. The candidates **with out ADMIT SLIPS Shall not be allowed** to appear in the Examination.
4. Eligible candidates are advised to read carefully the **INSTRUCTIONS** written on **ADMIT SLIP** for strict compliance.
5. Candidates must bring their own ink pencil and penholder.

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**YOUNIS JOYO**  
0333-2785569