



# Galaxies of Who's Who in Pakistan 2011

BOOK OF ACHIEVEMENTS &  
A MODEL SEED-BED FOR HUMAN RESOURCE DEVELOPMENT

A PROJECT OF



## CERTIFICATE OF EXCELLENCE

AWARDED TO

ABDUL SATTAR LARIK, PROF. DR. ENGR.

CATEGORY

MEN

In recognition of his/her outstanding achievements  
In the field of

ELECTRICAL ENGINEERING

Syed Mohammad Mahmud

Chairman

South Asia Publications (SAP)

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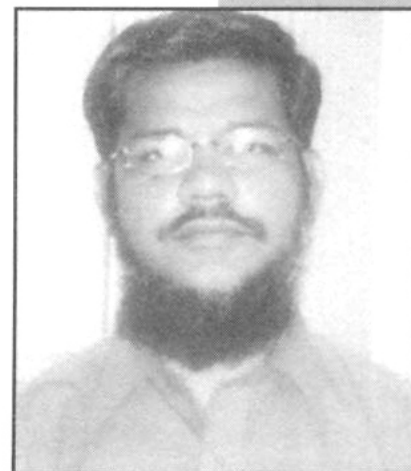


**ABDUL SATTAR LARIK, PROF. DR. ENGR.**  
Electrical Engineering  
Jamshoro

## Star Laureate 2011

### Recommendation

Mehran University of Engg. & Technology  
Jamshoro



Professor and Chairman, Department of Electrical Engineering at Mehran University of Engineering & Technology (MUET), Jamshoro, Prof. Dr. Engr. Abdul Sattar Larik is a young technocrat involved in teaching a number of courses both at undergraduate and postgraduate level for the last thirteen years.

Dr. Abdul Sattar Larik received his B.E (Electrical) degree in 1999, M.E (Electrical Power Engg.) in 2005 from Mehran University of Engineering & Technology, Jamshoro. In June 2009 he was awarded with Ph. D. in Power Electronics and Control Engineering under the Indigenous Ph. D. Program. Brilliant and outstanding, he also got Second Position in BE (Electrical) Degree Course at MUET, Jamshoro. He got the "Authors Shied" awarded by Governor Sindh & Chancellor, MUET on the occasion of 9th Convocation of the University (2006).

An avowed teacher and researcher, Dr. Larik has his research interest in the areas of Modeling and Simulation of Power Electronics Converters, Sliding Mode Controller, FACTS and Power Quality. His Final Year BE Thesis was on "Power Electronics and its Applications Literature Review, Laboratory Experimentation and Project Design". His ME Thesis was on "A Novel Solid-State Control System for the Minimization of Re-switching Transient Currents of Induction Motor". And the Title of his Ph. D. Thesis was "Analysis and Control of Dual Active Bridge DC-DC Converter Topologies".

Dr. Engr. Larik has more than 20 research papers at national and international level and is main author/co-author of more than a



dozen of books on "Power Electronics & Application", "Power Electronics", "Digital Electronics & Fuzzy Logic", "Microprocessor Systems", "Applied Electronics", "Basic Electrical Engineering" and "Electronic Devices & Circuits". He is also Life Member of Pakistan Engineering Council (PEC).

Born on 01st February 1973 in the village of Chhajra, Dr. Larik received his primary education in his native village. He passed his Matriculation (Science) from Govt. High School, Ratodero, Larkana (1990) and Intermediate (Pre-Engineering) from Govt. Degree College, Ratodero, Larkana (1992), both in First Division. Dr. Engr. Larik wants "To establish a Research Base at MUET which can promote Research Culture in Pakistan". His father Rasool Bux Larik is Retired from Agriculture Department.

Message: To conduct the research which bridges the gap between University & Industries.